MR. FARRIS.

PLEASE FIND ENCLOSED CHECK NO. 888 FOR THIRTY—FIVE DOLLARS TO PURCHASE THE MUNCHER CARTRIDGE AND TO COVER THE FIVE DOLLAR BANK FEE FOR PREVIOUS BOUNCED CHECK. I CAN ASSURE YOU THAT THERE WILL BE NO PROBLEM CASHING THIS ONE. MY APOLOGIES FOR THE INCONVENIENCE, THE BAD CHECK WAS NOT INTENTIONAL.

IF IT IS OF ANY CONSOLATION, HERE IS A SCHEMATIC DIAGRAM TO ENABLE THE ADDITION OF IK STATIC RAM TO THE ASTROCADE, PLEASE FEEL FREE TO USE AT YOUR DISGRESSION. THE MAIN COMPONENTS REQUIRED APE:

A) TWO 2114 4K RAM CHIPS

B) ONE 7430 TTL IC 8-INPUT POSITIVE NAND GATE C) ONE 7404 14-PIN HEX INVERTER

D) ONE 5 VOLT REGULATOR

- E) TWO 18-PIN WIRE WRAP SOCKETS
- F) TWO 14-PIN WIRE WRAP SOCKETS

G) WIRE WRAP WIRE

H) PERF BOARD (APPROX. 4" X 4")

I) ARCADE 50-PIN CONNECTOR

J) 4" TO 8" OF RIBBON CABLE (OPTIONAL)

K) SMALL BOX TO HOUSE THE UNIT

PLEASE NOTE! I HAVE AS YET TO FIND AND OBTAIN THE 50-PIN CONNECTOR, SO THIS MEMORY EXPANSION CARD HAS NOT BEEN TESTED. THE SCHEMATIC REPRESENTED IS ILLUSTRATED FOR THE NON-TECHNICIAN, AND MAS DERIVED FROM MARK KELLER'S SCHEMATIC IN ARCADIAN VOL. 4 NUMBER 2, ON PAGE 17.

SHOULD YOU FIND THIS WORTHY OF PUBLISHING IN THE ARCADIAN, CREDIT WOULD BE APPRECIATED, BUT I WON'T CRY IF YOU DON'T (GIVE

CREDIT).

I HAVE ENJOYED YOUR PUBLICATION, AND ALWAYS LOOK FORWARD TO THE NEXT ISSUE, AND AS YOUR RECORDS INDICATE, I HAVE SUBSCRIED TO ALL VOLUMES OF THE ARCADIAN AND INTEND TO CONTINUE. I HOPE THAT THE PREVIOUS BAD CHECK DOES NOT ESTABLISH ME AS A CREDIT RISK, FOR I WILL BE INTERESTED IN FUTURE PURCHASES WHEN INTERESTED.

GARY D. HUSTON
JULY 27, 1983

3715A MTVERTON PL

LIMA OH

